14   Memory C and Memory Z each contains an additional default, last

15   memory location initialized to zero which serves as a padding

16   value to said system bus of 32 bits, when the respective default,

17   last memory location of a respective memory slice is accessed by
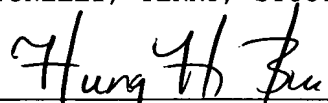
18   said Micro-Engine. --

REMARKS

Claims 1-24 are pending in the application. New claims 21-24 are added to alternatively define the process of designing the context memory of the host channel adapter.

To the extent necessary, applicants petition for an extension of time under 37 C.F.R. §1.136. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 01-2135 of Antonelli, Terry, Stout & Kraus, LLP (referencing Attorney Docket No. 219.38760X00), and please credit any overpayment of fees to said deposit account.

Respectfully submitted,

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